

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Tetsuo KAWANO, et al.

Serial No. 10/807,286

Group Art Unit: 2825

Confirmation No. 3924

Filed: March 24, 2004

Examiner: LAM, NELSON C

For: SEMICONDUCTOR INTEGRATED CIRCUIT TIMING ANALYSIS APPARATUS, TIMING ANALYSIS METHOD AND TIMING ANALYSIS PROGRAM (AS AMENDED)

**AMENDMENT**

**Mail Stop Amendment**

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

Sir:

This is in response to the non-final Office Action mailed May 19, 2006, and having a period for response set to expire on August 19, 2006. A Petition for a Two-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to October 19, 2006.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.